Fig.1

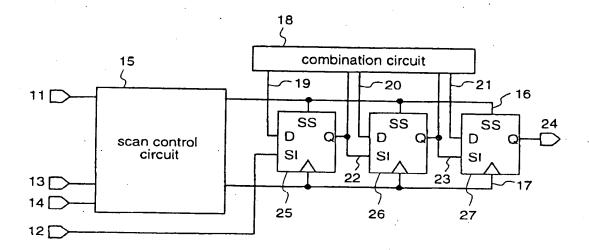
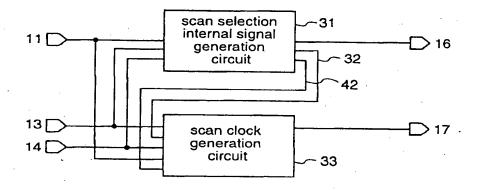


Fig.2



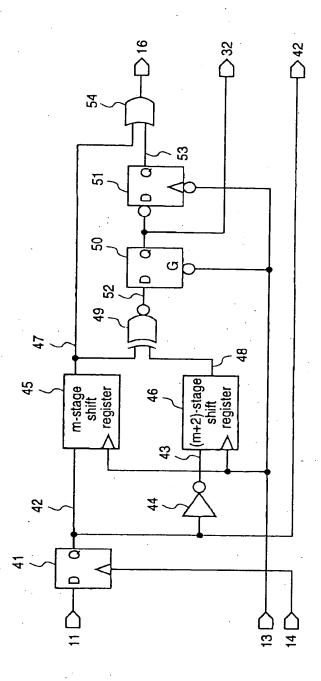


Fig.3

Toshinobu Nakao, Shinji Ozaki and Tomohisa Sezaki
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For: SCAN TEST CONTROL METHOD AND SCAN TEST CIRCUIT
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Fig.4

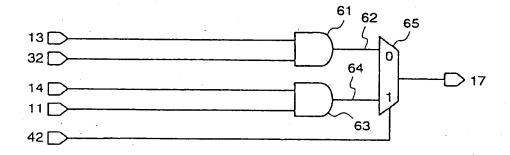


Fig.5

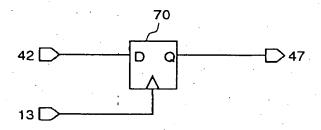
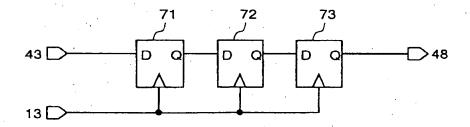


Fig.6



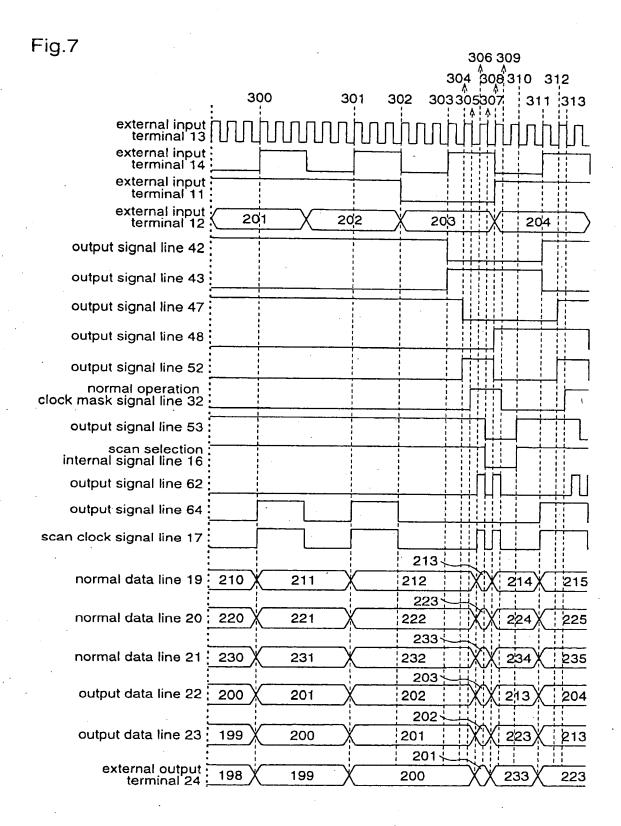
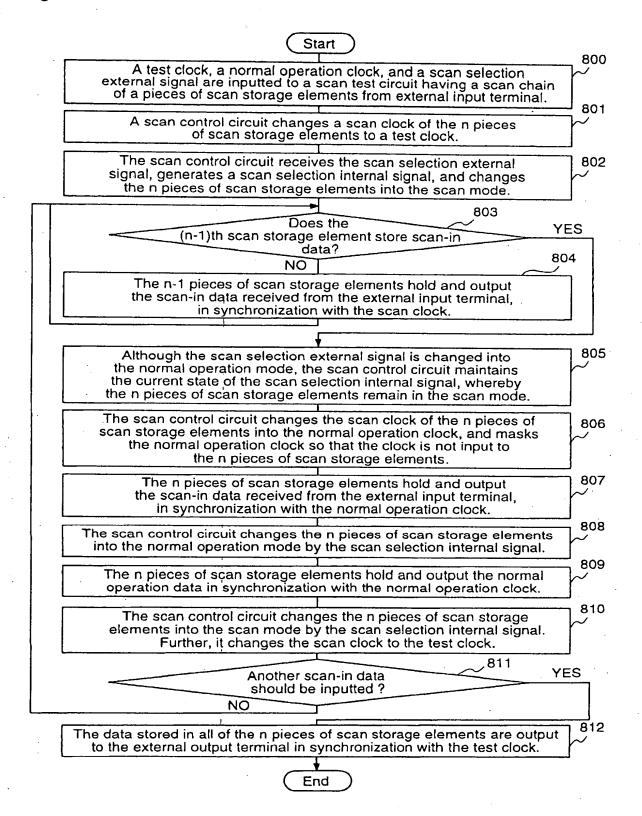
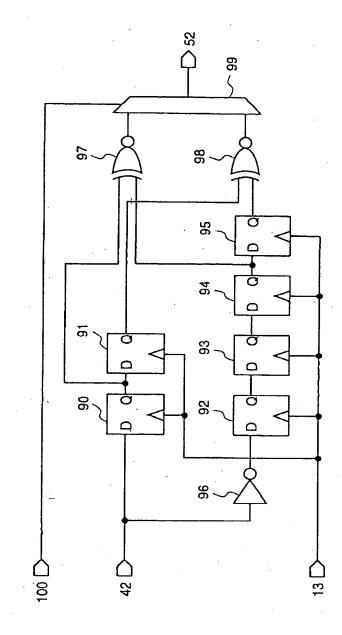


Fig.8





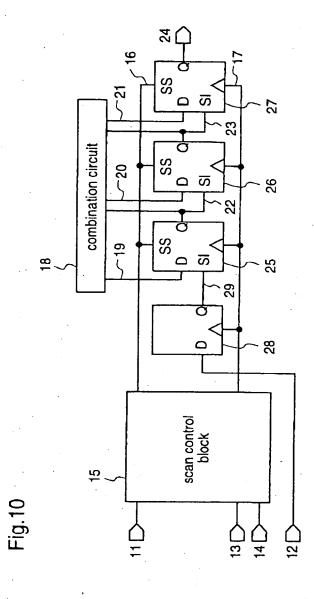
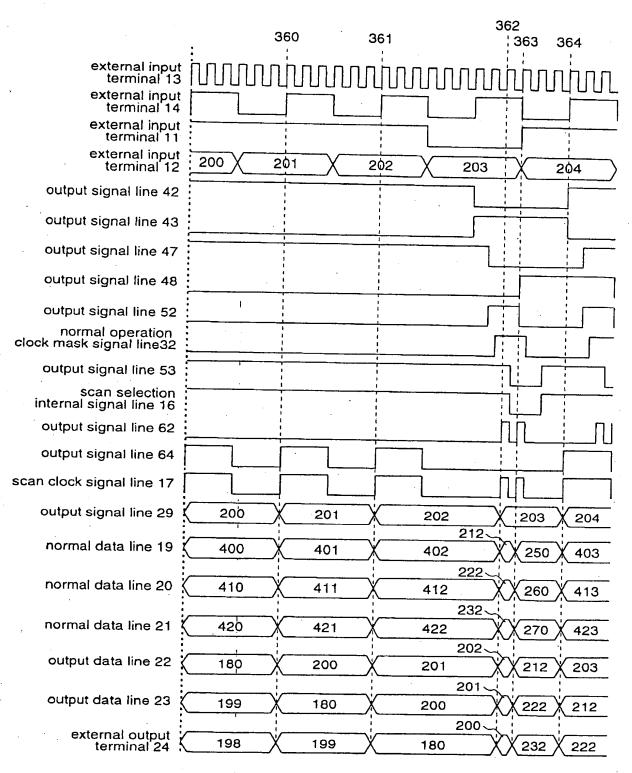
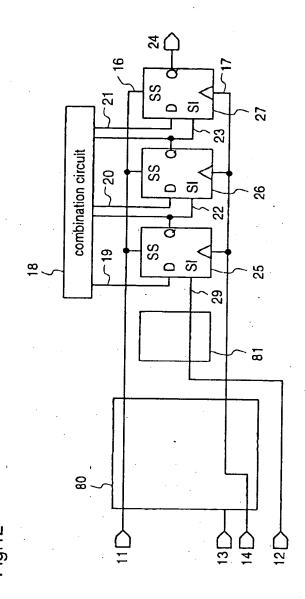


Fig.11





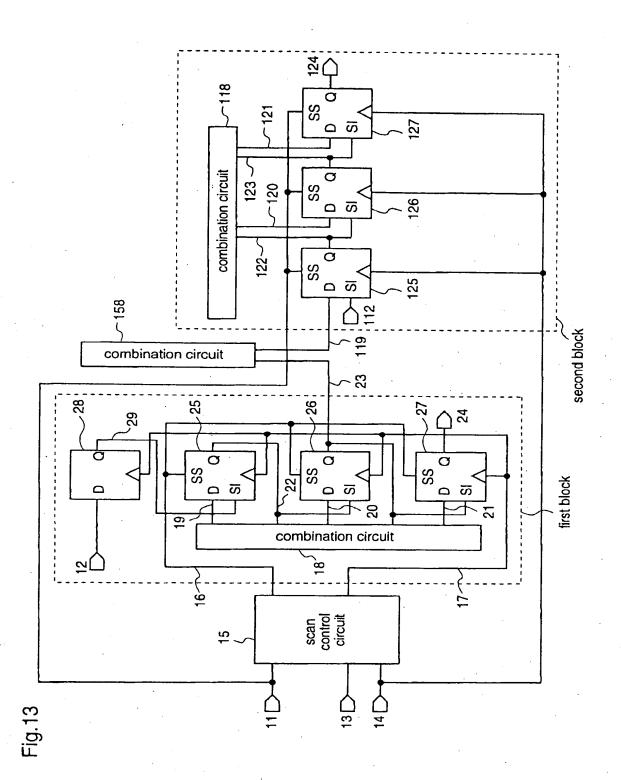
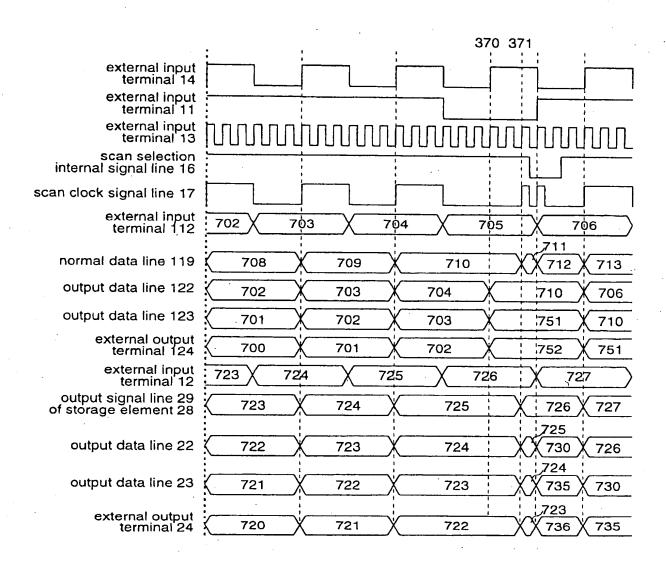


Fig.14



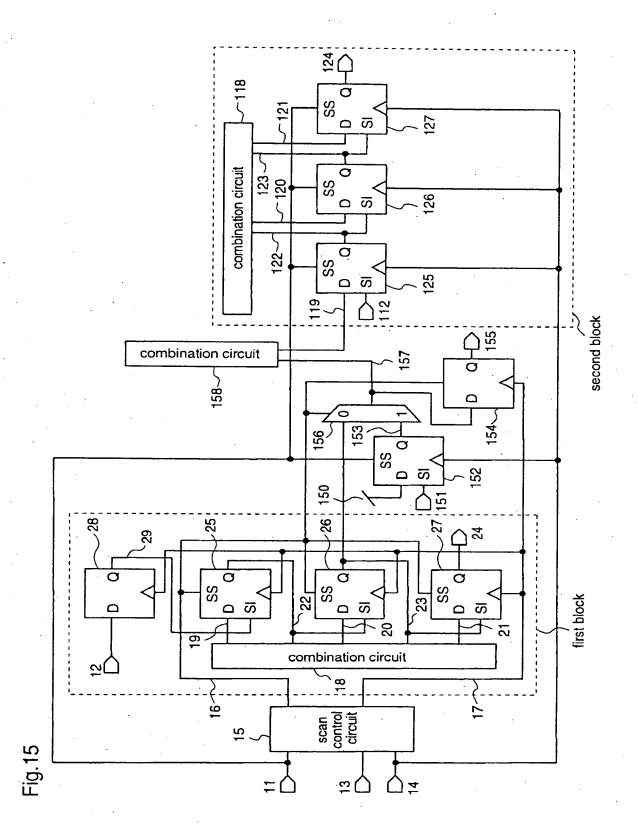


Fig.16

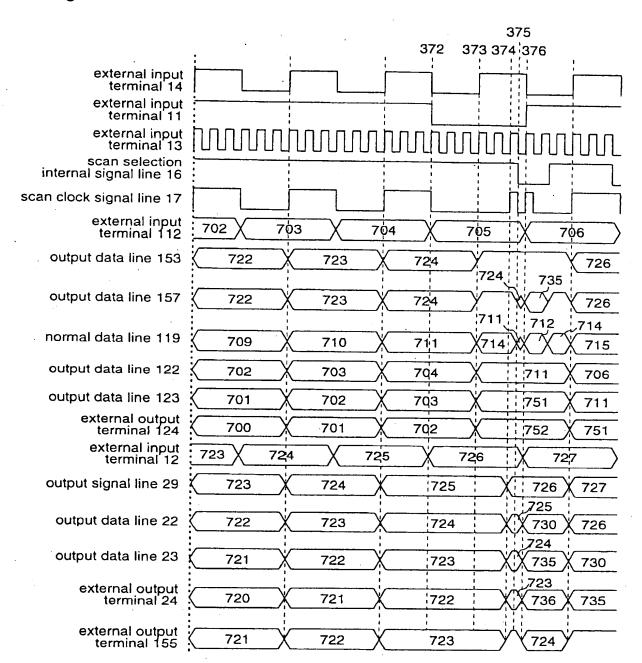


Fig.17

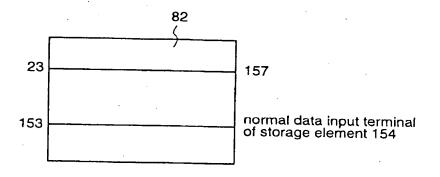


Fig.18 Prior Art

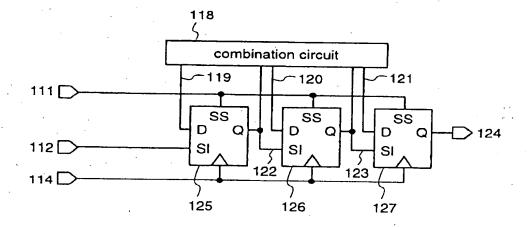


Fig.19 Prior Art

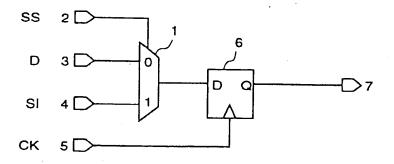


Fig.20 Prior Art

